An important role in neural computation. To build machines that can learn from experience, we need to understand the basic principles of how nervous systems work. Analogue electronics, such as those used in the VLSI 'integrate-and-fire' neuron with frequency adaptation, are crucial in this process.

**Analogue VLSI 'integrate-and-fire' neuron with frequency adaptation**

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**Indexing terms:** Analogue circuits, Brain models, Neural chips

A silicon neuron with separated 'integrate' and 'fire' representations is presented. The neuron is simple and requires a modest number of transistors, but preserves a number of temporal properties of physiological neurons: temporal integration, passive conductance, after-hyperpolarisation, absolute refractory period, and frequency adaptation.

**Introduction:** Biological nervous systems have evolved to function in a dynamic environment. Therefore, temporal aspects must play an important role in neural computation. To build machines which function in the same dynamic environment, much can be learned from the rich temporal behaviour of biological neurons. A step in this direction is the construction in silicon of model neuronal elements which can compute in real time.

Mahowald and Douglas [1] reported a circuit which modelled specific ionic conductances to produce in vitro discharge patterns very similar to a neocortical pyramidal neuron. Sarapeshkar, Watts and Mead [2] presented a simple (eight transistor) neuron which incorporated a mechanism for the absolute refractory period of the neuron, but without adaptation of the firing rate for successive firings. Both of these circuits model the potential 'at the axon hillock' of the neuron, i.e. where both the summed postsynaptic potentials and the 'sodium-spike' (action potential) are present in the membrane potential.

There is a strong motivation for representing separately the potential prior to the axonal trigger zone and the action-potential. This is needed for implementing physiologically-motivated plasticity (learning) mechanisms at the synapses of the neuron. Biophysical results indicate that the postsynaptic event required for associative learning, at least in the long-term potentiation (LTP) form of plasticity, depends on a 'consequence of dendritic depolarisation other than the elicitation of a sodium spike' [3]. A second motivation for a new neuronal circuit is the simple implementation of adaptation of the firing frequency, based on the assumption that this is important in neural dynamical behaviour [4]. We present a circuit that fulfils both criteria.

![Fig. 1 Circuit diagram for MOS 'integrate-and-fire' neuron](image)

**Description of neuron circuit:** The integrative function of the neuron (Fig. 1) occurs on the capacitor $C_{mem}$, which represents the 'membrane capacitance' of the neuron. Current is injected either by a synapse, or directly as might occur in an experimental situation, to produce the 'membrane potential' $V_{mem}$. Passive characteristics of the membrane are determined by the leakage transistor $M_{leak}$, which, although nonohmic in nature, is sufficient for the task.

Rather than mimicking the physiological workings of voltage-gated ion channels, their effect is considered functionally, so as to best take advantage of the medium of implementation. $V_{mem}$ generates an exponentially increasing channel current in $M_{leak}$. This current is compared at node AP with that generated by the (MOS subthreshold) action potential threshold, to produce an all-or-nothing action potential. The action potential injects a current determined by $V_{mem}$ into $C$. When the voltage on $C$ builds up enough to switch the comparator $M_{comp}$, the 'membrane potential' is pulled back down to the reset potential $V_{reset}$ through $M_{reset}$. $V_{reset}$ may range between small negative (after-hyperpolarising) voltages and the action potential threshold. This feedback loop plays a second role: for a period after the spike equivalent to the absolute refractory period of the neuron, while the comparator 'switch' remains on, no amount of injected current will induce the neuron to fire.

Frequency adaptation is implemented using an additional negative feedback loop around the firing mechanism. The action...
potential also injects current into $C_m$, which together with $M_m$ forms a leaky integrator. The voltage on the capacitor modulates another 'membrane conductance'. This conductance is comparable to the calcium-dependent potassium conductance, and the voltage on $C_m$ comparable to the intracellular calcium concentration of neurophysiology.

results are qualitatively similar to those presented for the saturation of the current-to-frequency transduction curve with spiking causing the frequency adaptation effect. Fig. 3 shows the adaptation of the current-to-frequency transduction curve with spiking activity. The results are qualitatively similar to those presented for a theoretical 'integrate-and-fire' neuron [4], and to data from physiological recordings [5].

Fig. 2 Response of 'membrane voltage', action potential, and 'intracellular calcium concentration' voltage to step of injected current of 100pA

\[ \begin{align*}
V_m & = 0.60V, \\
V'_m & = 0.30V, \\
V''_m & = 0.2V, \\
V_A & = 0.70V, \\
V''_A & = 0.60V, \\
V_\text{spk} & = 0.55V \\
\text{with} & = 0.2V \text{(SPICE results)}
\end{align*} \]

Fig. 3 Firing frequency against injected current

a Adaptation of firing frequency with successive spikes
b Saturated firing frequency curves for uniform distribution of parameters in 20mV band around values given in text

c Instantaneous frequencies for each succeeding inter-spike interval, from top left to lower right

The parameter settings used to obtain these figures were: $V_m = 0.60V$, $V'_m = 0.30V$, $V''_m = 0.2V$, $V_A = 0.70V$, $V''_A = 0.60V$, $V_\text{spk} = 0.55V$ and $V_\text{spk} = 0.2V$ (SPICE parameters for the Orbit 1.2um well process were used). The system appeared to be functionally robust to variations in these parameters, as is illustrated in Fig. 3b. Owing to the 'all-or-nothing' nature of the action potential, mismatch of transistors will produce variation in the temporal characteristics rather than failure.

Conclusions: The neuronal circuit presented is simple but preserves many of the essential temporal characteristics of biological neurons. It will provide a suitable element for studying dynamical issues in neural networks, such as rhythmic activity. Owing to the representation of the 'membrane voltage' just prior to the axon hillock, it also prompts research into silicon 'plasticity' mechanisms capable of associative learning.

Acknowledgments: The authors wish to thank B. Flower for helpful suggestions and comments on an earlier draft of this Letter.

© IEE 1995 22 May 1995
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References

Neural network restoration of images suffering space-variant distortion

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Indexing terms: Image reconstruction. Neural networks

A neural network algorithm for the restoration of images suffering space-variant distortion is introduced. Using multiple weighting matrices to represent space-variant, the algorithm provides high quality restorations in a computationally inexpensive fashion.

Introduction: Images recorded in many practical applications suffer space-variant distortions. It is well known that correction of such distortions is very difficult. The techniques available are either extremely computationally intensive (space-domain processing) and/or sensitive to the conditions of the distortion (space-invariant approximation).

We present a neural computing algorithm for restoring images degraded by space-variant distortion. Experimental results show that the proposed algorithm is able to provide high quality restorations efficiently.

Restoration model: Considering an $M \times M$ input image, in most cases the image degradation model is a linear distortion described by the equation [1, 2]

\[ y = Hf + n \]

where $f$ and $g$ are lexicographically organised original and degraded image vectors, respectively, of size $M^2 \times 1$, $H$ is a matrix distortion operator and $n$ is an additive noise vector. This model is used here to describe images degraded by a space-variant distortion. We consider a simple form of space-variant distortion. The